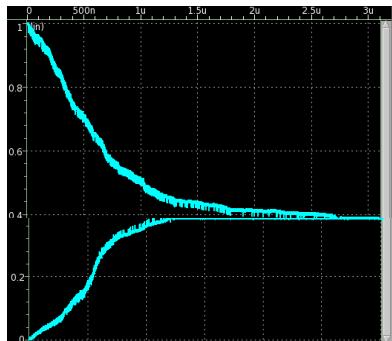
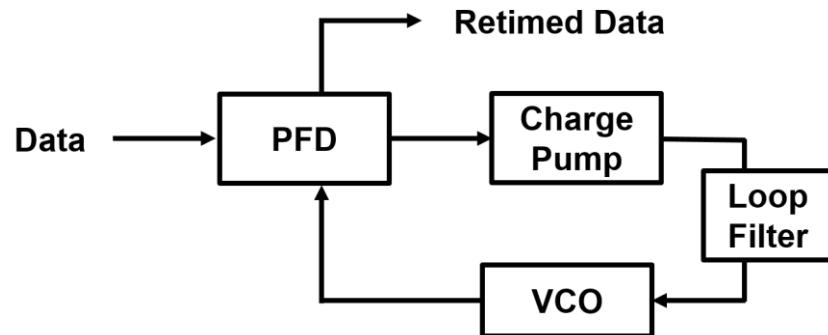
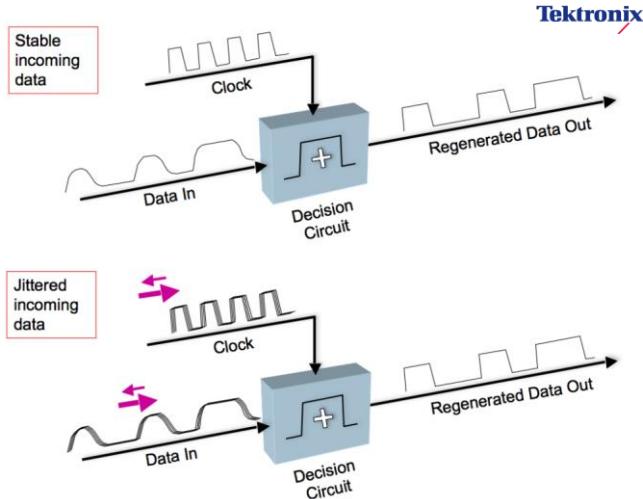
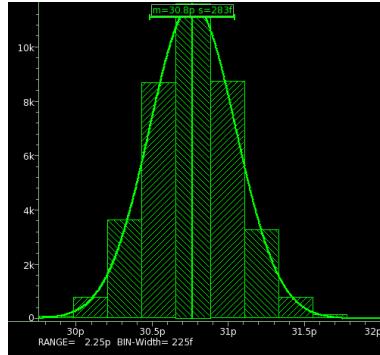


# High-speed CDR circuit



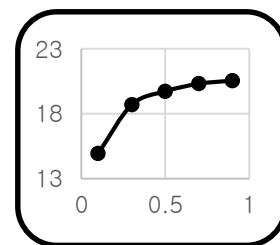
Control voltage vs time



Jitter @ locked

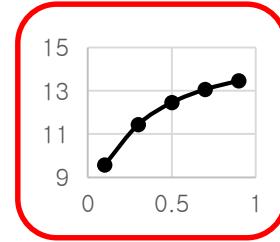
fullrate

0° 20GHz  
90° 20GHz



halfrate

45° 10GHz  
135° 10GHz



# 2019 Works

